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(54) **Transmission circuit.**

(57) In the transmission circuit, part of the output signal of the transmission circuit is derived by an output monitor circuit (12), and, after being subjected to envelope detection by an output detector circuit (13), the output signal of the output detector circuit is inputted to a peak hold circuit (14) successively holding the peaks of the input signal, so that varying amplitude components are removed, and a DC value is detected. An error detector circuit (15) compares the detected value with a reference signal generated

from a reference signal generator circuit (16), and the error signal is used as a gain control signal to control the gain of a power amplifier circuit (11).

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## BACKGROUND OF THE INVENTION

## Field of the Invention

This invention relates to a transmission circuit for use in a radio transmitter, such as, that used in the field of mobile radio communication.

## Description of the Related Art

Fig. 1 is a block diagram showing the structure of a prior art transmission circuit. Referring to Fig. 1, the prior art transmission circuit includes a power amplifier circuit 1, an output monitor circuit 2, an output detector circuit 3, an error detector circuit 4, a reference signal generator circuit 5, a high-frequency signal input terminal 6, a gain control terminal 7 and a transmission output terminal 8.

The operation of the prior art transmission circuit having the above structure will now be described. A high-frequency signal inputted through the high-frequency signal input terminal 6 is amplified by the power amplifier circuit 1, and, after being then branched by the output monitor circuit 2, part of the amplified high-frequency signal is outputted from the transmission output terminal 8. The remaining part of the amplified high-frequency signal is inputted to the output detector circuit 3, and the output signal  $V_{DET}$  of the output detector circuit 3 is inputted to the error detector circuit 4. In the error detector circuit 4, the detected voltage signal  $V_{DET}$  is compared with a reference signal  $V_{REF}$  generated from the reference signal generator circuit 5 to detect the error voltage, and the detected error voltage signal is then inputted to the power amplifier circuit 1 through the gain control terminal 7 so as to control the gain of the power amplifier circuit 1.

However, in the case where the high-frequency signal inputted through the high-frequency input terminal 6 of the transmission circuit having the structure as described above is a linear modulated signal whose amplitude carries some kind of information, this manner of loop control will cancel the modulation when the reference signal  $V_{REF}$  is a constant voltage. Thus, the prior art transmission circuit has had the problem that its output waveform tends to be distorted.

## SUMMARY OF THE INVENTION

With a view to solve such a prior art problem, it is an object of the present invention to provide a transmission circuit which can transmit a high-frequency signal with satisfactory transmission performance and without giving rise to undesirable distortion of its output waveform attributable to the loop control even when the reference signal input-

ted to its error detector circuit is a constant voltage.

The transmission circuit according to the present invention which attains the above object comprises an output detector circuit subjecting part of the output signal of the transmission circuit to envelope detection, a peak hold circuit successively holding the peaks of the output signal of the output detector circuit, and an error detector circuit comparing the output signal of the peak hold circuit with a reference signal to generate the error signal used for controlling the gain of a power amplifier circuit.

Thus, in the transmission circuit according to the present invention, part of the output signal of the transmission circuit is subjected to envelope detection by the output detector circuit, and the output signal of the output detector circuit is inputted to the peak hold circuit successively holding the peaks of the output signal of the output detector circuit, so that the peak level of the output signal of the transmission circuit is detected as a DC value. Therefore, even when the reference signal is a constant voltage, the modulation would not be cancelled by the loop control, and the transmission circuit can operate to exhibit the desired satisfactory transmission performance.

## BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram showing the structure of a prior art transmission circuit.

Fig. 2 is a block diagram showing the structure of a first embodiment of the transmission circuit according to the present invention.

Figs. 3A to 3E show waveforms of various signals appearing in the first embodiment of the transmission circuit according to the present invention.

Fig. 4 is a block diagram showing the structure of a second embodiment of the transmission circuit according to the present invention.

Fig. 5 is a block diagram showing the structure of a third embodiment of the transmission circuit according to the present invention.

Fig. 6 is a block diagram showing the structure of a fourth embodiment of the transmission circuit according to the present invention.

Figs. 7A to 7F show waveforms of various signals appearing in the fourth embodiment of the transmission circuit according to the present invention.

Fig. 8 is a block diagram showing the structure of a fifth embodiment of the transmission circuit according to the present invention.

Figs. 9A to 9C show waveforms of various signals appearing in the fifth embodiment of the transmission circuit according to the present invention.

Fig. 10 is a block diagram showing the structure of a sixth embodiment of the transmission circuit according to the present invention.

Fig. 11 is a block diagram showing the structure of a seventh embodiment of the transmission circuit according to the present invention.

Figs. 12A to 12D show waveforms of various signals appearing in the seventh embodiment of the transmission circuit according to the present invention.

Fig. 13 is a block diagram showing the structure of an eighth embodiment of the transmission circuit according to the present invention.

Fig. 14 is a block diagram showing the structure of a ninth embodiment of the transmission circuit according to the present invention.

Figs. 15A to 15E show waveforms of various signals appearing in the ninth embodiment of the transmission circuit according to the present invention.

Fig. 16 is a block diagram showing the structure of a tenth embodiment of the transmission circuit according to the present invention.

Figs. 17A to 17E show waveforms of various signals appearing in the tenth embodiment of the transmission circuit according to the present invention.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

A first embodiment of the present invention will now be described by reference to the drawings. Fig. 2 is a block diagram showing the structure of the first embodiment of the transmission circuit according to the present invention, and Figs. 3A to 3E show waveforms of various signals appearing in the transmission circuit shown in Fig. 2.

Referring to Fig. 2, the transmission circuit comprises a power amplifier circuit 11 amplifying a high-frequency signal 21, an output monitor circuit 12 deriving part of the output signal of the transmission circuit, an output detector circuit 13 subjecting a monitor signal, which is the output signal of the output monitor circuit 12, to envelope detection, a peak hold circuit 14 successively holding the peaks of a detector output signal 22, an error detector circuit 15 comparing the output signal of the peak hold circuit 14 with a reference signal  $V_{REF}$  generated from a reference signal generator circuit 16 thereby generating the error voltage, a gain control terminal 17 of the power amplifier circuit 11, a high-frequency signal input terminal 18, and a transmission output terminal 19.

The operation of the first embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 2 together with Figs. 3A to 3E. A high-

frequency signal 21 having a waveform as shown in Fig. 3A is inputted through the high-frequency signal input terminal 18 to the power amplifier circuit 11 and, after being amplified by the power amplifier circuit 11 and then passed through the output monitor circuit 12, appears at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 to be inputted as the monitor signal to the output detector circuit 13 and, after being detected by the output detector circuit 13, appears as the detector output signal 22 having a waveform as shown in Fig. 3B. The detector output signal 22 is inputted to the peak hold circuit 14 generating a peak hold signal 23 successively holding the peaks of the detector output signal 22 and having a waveform as shown in Fig. 3C. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 22, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator circuit 16 generates the reference signal 24 having a waveform as shown in Fig. 3D so as to set the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects an error voltage by comparing the peak hold signal 23 with the reference signal 24 and, after amplifying the error voltage, outputs the error voltage signal. This error voltage signal is inputted as a gain control signal 25 having a waveform as shown in Fig. 3E to the power amplifier circuit 11 through the gain control terminal 17 so as to control the gain of the power amplifier circuit 11.

It will be seen from the above description of the first embodiment of the present invention that, even when the high-frequency input signal may be a modulated wave signal having a varying amplitude, the gain control signal 25 for controlling the gain of the power amplifier circuit 11 is substantially maintained at a constant level, and undesirable distortion of the output waveform attributable to the loop control would not occur, so that the desired satisfactory transmission performance can be exhibited.

Fig. 4 is a block diagram showing the structure of a second embodiment of the transmission circuit according to the present invention. This second embodiment is a partial modification of the first embodiment shown in Fig. 2, and the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 2 so as to dispense with repetition of the same description. This second embodiment differs from the first embodi-

ment in that a variable high-frequency attenuator 31 having an attenuation control terminal 32 is connected between the output monitor circuit 12 and the output detector circuit 13.

The operation of the second embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 4 together with Figs. 3A to 3E. A high-frequency signal 21 is inputted through the high-frequency signal input terminal 18 to the power amplifier circuit 11 and, after being amplified by the power amplifier circuit 11 and then passed through the output monitor circuit 12, appears at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 to be inputted as the monitor signal to the variable high-frequency attenuator 31 and, after being attenuated by a predetermined amount by the attenuation control signal inputted through the attenuation control terminal 32, inputted to the output detector circuit 13 to be detected. The detector output signal 22, which is the output signal of the output detector circuit 13, is inputted to the peak hold circuit 14 generating the peak hold signal 23 successively holding the peaks of the detector output signal 22. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 22, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator circuit 16 generates the reference signal 24 so as to set the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects the error voltage by comparing the peak hold signal 23 with the reference signal 24 and, after amplifying the detected error voltage, outputs the error voltage signal. This error voltage signal is inputted as the gain control signal 25 to the power amplifier circuit 11 through the gain control terminal 17 so as to control the gain of the power amplifier circuit 11.

It will be seen from the above description of the second embodiment of the present invention that the variable high-frequency attenuator 31 is connected between the output monitor circuit 12 and the output detector circuit 13. Therefore, when, for example, the amount of attenuation by the variable high-frequency attenuator 31 is selected to increase in a relation corresponding to an increase in the value of the output signal of the transmission circuit, the power inputted to the output detector circuit 13 can be maintained at the same level regardless of a change in the value of the output signal of the transmission circuit, so that the range

$\Delta P$  of the output signal of the transmission circuit that can be detected within a detected voltage range  $\Delta V$  can be widened. That is, regardless of a change in the value of the output signal of the transmission circuit, the proper detected voltage can be always obtained, so that the desired satisfactory loop control can be attained.

Fig. 5 is a block diagram showing the structure of a third embodiment of the transmission circuit according to the present invention. The third embodiment shown in Fig. 5 is a partial modification of the second embodiment shown in Fig. 4, and the same reference numerals are used to designate the same or equivalent parts so as to dispense with repetition of the same description. Waveforms of various signals are the same as those shown in Figs. 3A to 3E. This third embodiment differs from the second embodiment in that a high-frequency amplifier 41 is connected between the variable high-frequency attenuator 31 and the output detector circuit 13.

The operation of the third embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 5 together with Figs. 3A to 3E. A high-frequency signal 21 is inputted through the high-frequency input terminal 18 and, after being amplified by the power amplifier circuit 11 and then passed through the output monitor circuit 12, appears at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 to be inputted as the monitor signal to the variable high-frequency attenuator 31 and, after being attenuated by a predetermined amount by the control signal inputted through the attenuation control terminal 32 and amplified by the high-frequency amplifier 41, inputted to the output detector circuit 13 to be detected. The detector output signal, which is the output signal 22 of the output detector circuit 13, is inputted to the peak hold circuit 14 generating the peak hold signal 23 successively holding the peaks of the detector output signal 22. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 22, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator circuit 16 generates the reference signal 24 so as to set the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects the error voltage by comparing the peak hold signal 23 with the reference signal 24 and, after amplifying the detected error voltage, outputs the error voltage signal. This error voltage signal is

inputted as the gain control signal 25 to the power amplifier circuit 11 through the gain control terminal 17 so as to control the gain of the power amplifier circuit 11.

It will be seen from the above description of the third embodiment of the present invention that the connection of the high-frequency amplifier 41 between the variable high-frequency attenuator 31 and the output monitor circuit 12 is advantageous in that the transmission circuit can also deal with the case where the monitor signal inputted from the output monitor circuit 12 to the variable high-frequency attenuator 31 is small.

Fig. 6 shows the structure of a fourth embodiment of the transmission circuit according to the present invention when the present invention is applied to a booster unit adapted to be connected to a portable radio telephone set operable as an independent device so as to amplify a high-frequency signal transmitted from the portable radio telephone set. Figs. 7A to 7F show waveforms of various signals appearing in the transmission circuit. In Fig. 6, the reference numerals 51, 52 and 53 designate the portable radio telephone set, the booster unit, and a power supply control terminal connected to a power amplifier circuit 11 in the booster unit 52, respectively and, as for the remaining parts, the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 2 so as to dispense with repetition of the same description.

The operation of the fourth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 6 together with Figs. 7A to 7F. A high-frequency signal 61 having a waveform as shown in Fig. 7A is inputted through the high-frequency signal input terminal 18 to be amplified by the power amplifier circuit 11 and is then passed through the output monitor circuit 12 to appear at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 and is inputted as the monitor signal to the output detector circuit 13 to be detected. A detector output signal 62 having a waveform as shown in Fig. 7B is inputted to the peak hold circuit 14 generating a peak hold signal 63 successively holding the peaks of the detector output signal 62 and having a waveform as shown in Fig. 7C. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 62, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator

circuit 16 generates a reference signal 64 having a waveform as shown in Fig. 7D so as to set the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects an error voltage by comparing the peak hold signal 63 with the reference signal 64 and outputs the detected error voltage as an error voltage signal. This error voltage signal is inputted as a gain control signal 65 having a waveform as shown in Fig. 7E to the power amplifier circuit 11 through the gain control terminal 17 so as to control the gain of the power amplifier circuit 11. At this time, a power supply control signal 66 having a waveform as shown in Fig. 7F and corresponding to a burst-like high-frequency signal is inputted from the portable radio telephone set 51 through the power supply control terminal 53 connected to the power amplifier circuit 11 so as to turn on/off the output signal of the transmission circuit thereby minimizing consumption of unnecessary power.

Thus, according to the fourth embodiment of the present invention, the power supply control terminal 53 is additionally connected to the power amplifier circuit 11, and the output signal of the transmission circuit is on/off controlled by the burst-like power supply control signal 66, so that consumption of unnecessary power can be minimized. In the case of this fourth embodiment too, an attenuator similar to the variable high-frequency attenuator 31 used in the second embodiment may be connected between the output monitor circuit 12 and the output detector circuit 13 or the high-frequency amplifier 41 used in the third embodiment may be connected to such an attenuator.

Fig. 8 is a block diagram showing the structure of a fifth embodiment of the transmission circuit according to the present invention when the present invention is applied to a booster unit adapted to be connected to a portable radio telephone set so as to amplify a high-frequency signal transmitted from the portable radio telephone set. Figs. 9A to 9C show waveforms of various signals appearing in the transmission circuit, and Figs. 7B to 7E are also applied to this fifth embodiment of the present invention. In Fig. 8, the reference numerals 71, 72, 73, 74 and 75 designate the portable radio telephone set, the booster unit, an input detector circuit, a timing generator circuit, and a power supply control terminal for controlling the power amplifier circuit 11, respectively and, as for the remaining parts, the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 2 so as to dispense with repetition of the same description.

The operation of the fifth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 8 together with Figs. 9A to 9C. A high-

frequency signal 81 having a waveform as shown in Fig. 9A is inputted through the high-frequency signal input terminal 18 to be amplified by the power amplifier circuit 11 and then passed through the output monitor circuit 12 to appear at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 to be inputted as the monitor signal to the output detector circuit 13 to be detected. The detector output signal 62, which is the output signal of the output detector circuit 13, is inputted to the peak hold circuit 14 generating the peak hold signal 63 successively holding the peaks of the detector output signal 62. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 62, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator circuit 16 generates the reference signal 64 which sets the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects the error voltage by comparing the peak hold signal 63 with the reference signal 64 and, after amplifying the detected error voltage, outputs the error voltage signal. This error voltage signal is inputted as the gain control signal 65 to the power amplifier circuit 11 through the gain control terminal 17 so as to control the gain of the power amplifier circuit 11.

Part of the high-frequency signal 81 inputted through the high-frequency signal input terminal 18 and then branched is inputted to the input detector circuit 73 to be detected. An input-detector output signal 82 having a waveform as shown in Fig. 9B is inputted to the timing generator circuit 74 which generates a burst-like output signal turned on/off at certain rising and falling timing of the waveform of the input-detector output signal 82. This on/off signal is inputted as a power supply control signal 83 having a waveform as shown in Fig. 9C to the power amplifier circuit 11 through the power supply control terminal 75 so as to control the power supply of the power amplifier circuit 11.

Thus, according to the fifth embodiment of the present invention, the burst-like power supply control signal 83 inputted to the power amplifier circuit 11 through the power supply control terminal 75 is generated on the basis of the high-frequency signal 81, so that the signal line extending from the portable radio telephone set 71 is unnecessary, and the above manner of power supply control can minimize consumption of unnecessary power.

In this fifth embodiment too, an attenuator similar to the variable high-frequency attenuator 31

used in the second embodiment may be connected between the output monitor circuit 12 and the output detector circuit 13 or the high-frequency amplifier 41 used in the third embodiment may be connected to such an attenuator.

Fig. 10 is a block diagram showing the structure of a sixth embodiment of the transmission circuit according to the present invention when the present invention is applied to a booster unit adapted for amplifying a high-frequency signal transmitted from the portable radio telephone set. In Fig. 10, the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 8 so as to dispense with repetition of the same description. Waveforms of various signals appearing in the transmission circuit are the same as those shown in Figs. 7A to 7F and Figs. 9A to 9C. This sixth embodiment differs from the fifth embodiment in that a high-frequency amplifier 91 is connected between the high-frequency signal input terminal 18 and the input detector circuit 73.

The operation of the sixth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 10 together with Figs. 7A to 7F and Figs. 9A to 9C. A high-frequency signal 81 inputted through the high-frequency signal input terminal 18 is amplified by the power amplifier circuit 11 and then passed through the output monitor circuit 12 to appear at the transmission output terminal 19 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 12 and is inputted as the monitor signal to the output detector circuit 13 to be detected. The detector output signal 62 is inputted to the peak hold circuit 14 to appear as the peak hold signal 63 successively holding the peaks of the detector output signal 62. The peak hold circuit 14 is composed of the combination of, for example, time constant circuits, and its time constant during charging is selected to be small so as to follow the detector output signal 62, while its time constant during discharging is selected to be large so as to hold the peak value. The reference signal generator circuit 16 generates the reference signal 64 which sets the reference level of the output signal of the transmission circuit. The error detector circuit 15 detects the error voltage by comparing the peak hold signal 63 with the reference signal 64 and, after amplifying the detected error voltage, outputs the error voltage signal. This error voltage signal is inputted as the gain control signal 65 to the power amplifier circuit 11 through the gain control terminal 17 connected to the power amplifier circuit 11 so as to control the gain of the power amplifier circuit 11.

Part of the high-frequency signal 81 inputted from the portable radio telephone set 71 through the high-frequency signal input terminal 18 and then branched is inputted, after being amplified by the high-frequency amplifier 91, to the input detector circuit 73 to be detected. The input-detector output signal 82 is inputted to the timing generator circuit 74 which generates the burst-like power supply control signal 83 turned on/off at certain rising and falling timing of the waveform of the input-detector output signal 82. This power supply control signal 83 is inputted to the power amplifier circuit 11 through the power supply control terminal 75 so as to control the power supply of the power amplifier circuit 11.

Thus, according to the sixth embodiment of the present invention, the connection of the high-frequency amplifier 91 between the high-frequency signal input terminal 18 and the input detector circuit 73 is advantageous in that the transmission circuit can also deal with the case where the high-frequency signal 81 is small and, because the low level at the leading edge of the burst signal can be detected, the leading edge of the burst signal can be detected earlier than heretofore so that the desired on/off control of the output signal of the transmission circuit can be reliably attained.

In this sixth embodiment too, an attenuator similar to the variable high-frequency attenuator 31 used in the second embodiment may be connected between the output monitor circuit 12 and the output detector circuit 13 or the variable high-frequency amplifier 41 used in the third embodiment may be connected to such an attenuator.

A seventh embodiment of the present invention will now be described by reference to the drawings. Fig. 11 is a block diagram showing the structure of the seventh embodiment of the transmission circuit according to the present invention, and Figs. 12A to 12D show waveforms of various signals appearing in the transmission circuit.

Referring to Fig. 11 showing application of the seventh embodiment of the transmission circuit of the present invention to a booster unit 113 adapted to be connected to a portable radio telephone set 114, the transmission circuit comprises a variable gain control circuit 101, a power amplifier circuit 102, an output monitor circuit 103, a variable high-frequency attenuator 104, an output detector circuit 105, an error detector circuit 106, an input detector circuit 107, an input monitor circuit 108, a high-frequency signal input terminal 109, a transmission output terminal 110, an attenuation control terminal 111 connected to the variable high-frequency attenuator 104, and a gain control terminal 112 of the variable gain control circuit 101.

The operation of the seventh embodiment of the transmission circuit of the present invention

having the above structure will now be described by reference to Fig. 11 together with Figs. 12A to 12D. A high-frequency signal 121 having a waveform as shown in Fig. 12A is inputted from the portable radio telephone set 114 through the high-frequency signal input terminal 109 and is branched by the input monitor circuit 108. The branched part of the high-frequency signal 121 is, after being amplified by the combination of the variable gain control circuit 101 and the power amplifier circuit 102, branched again by the output monitor circuit 103 to appear at the transmission output terminal 110 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 103 to be inputted as a monitor signal to the variable high-frequency attenuator 104, and, after being attenuated by a predetermined amount by a control signal inputted through the attenuation control terminal 111, inputted to and detected by the output detector circuit 105 to appear as an output-detector output signal 122 having a waveform as shown in Fig. 12B.

Also, part of the input signal 121 branched by the input monitor circuit 108 is inputted to and detected by the input detector circuit 107 to appear as an input-detector output signal 123 having a waveform as shown in Fig. 12C. The error detector circuit 106 detects an error voltage by comparing the output signal 122 of the output detector circuit 105 with the output signal 123 of the input detector circuit 107 using the latter output signal 123 as a reference signal, and, after amplifying the error voltage, outputs an error voltage signal. This error voltage signal is inputted as a gain control signal 124 having a waveform as shown in Fig. 12D to the variable gain control circuit 101 through the gain control terminal 112 so as to control the gain or attenuation.

Thus, the seventh embodiment of the present invention is advantageous in that, even when a modulated wave signal having a varying amplitude is inputted to the transmission circuit, the input signal is detected to provide a reference signal including varying amplitude components, and such a reference signal is used for error detection, so that the desired satisfactory transmission performance can be exhibited without occurrence of undesirable distortion of the output waveform attributable to the loop control.

Fig. 13 is a block diagram showing the structure of an eighth embodiment of the transmission circuit according to the present invention. In Fig. 13, the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 11 so as to dispense with repetition of the same description. Waveforms of various signals

appearing in the transmission circuit are the same as those shown in Figs. 12A to 12D. This eighth embodiment is a partial modification of the seventh embodiment and differs from the seventh embodiment in that a high-frequency amplifier 131 is connected between the variable high-frequency attenuator 104 and the output detector circuit 105.

The operation of the eighth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 13 together with Figs. 12A to 12D.

The high-frequency signal 121 inputted from the portable radio telephones set 114 through the high-frequency signal input terminal 109 and then passed through the input monitor circuit 108 is, after being amplified by the combination of the variable gain control circuit 101 and the power amplifier circuit 102, passed through the output monitor circuit 103 to appear at the transmission output terminal 110 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 103 to be inputted as the monitor signal to the variable high-frequency attenuator 104 and, after being attenuated by a predetermined amount by the control signal inputted through the attenuation control terminal 111 and amplified by the high-frequency amplifier 131, inputted to and detected by the output detector circuit 105 to appear as the output-detector output signal 122 which is the output signal of the output detector circuit 105. Also, part of the input signal 121 branched by the input monitor circuit 108 is inputted to and detected by the input detector circuit 107 to appear as the input-detector output signal 123 which is the output signal of the input detector circuit 107. The error detector circuit 106 detects the error voltage by comparing the output-detector output signal 122 with the input-detector output signal 123 using the latter output signal 123 as the reference signal and, after amplifying the error voltage, outputs the error voltage signal. This error voltage signal is inputted as the gain control signal 124 to the variable gain control circuit 101 so as to control the gain or attenuation.

Thus, the eighth embodiment of the present invention in which the high-frequency amplifier 131 is connected between the variable high-frequency attenuator 104 and the output detector circuit 105 is advantageous in that the transmission circuit can also deal with the case where the monitor signal outputted from the output monitor circuit 103 is small.

Fig. 14 is a block diagram showing the structure of a ninth embodiment of the transmission circuit according to the present invention, and Figs. 15A to 15E show waveforms of various signals

appearing in the transmission circuit. The ninth embodiment is a partial modification of the seventh embodiment shown in Fig. 11, and the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 11 so as to dispense with repetition of the same description. This ninth embodiment differs from the seventh embodiment in that a power supply control terminal 141 is additionally connected to the power amplifier circuit 102.

The operation of the ninth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 14 together with Figs. 15A to 15E. A high-frequency signal 151 having a waveform as shown in Fig. 15A is inputted from the portable radio telephone set 114 through the high-frequency signal input terminal 109 and then passed through the input monitor circuit 108. The output signal of the input monitor circuit 108 is, after being amplified by the combination of the variable gain control circuit 101 and the power amplifier circuit 102 and passed through the output monitor circuit 103, appears at the transmission output terminal 110 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 103 to be inputted as the monitor signal to the variable high-frequency attenuator 104 and, after being attenuated by a predetermined amount by the control signal inputted through the attenuation control terminal 111, inputted to and detected by the output detector circuit 105 to appear as an output-detector output signal 152 having a waveform as shown in Fig. 15B. Also, part of the input signal 151 branched by the input monitor circuit 108 is inputted to and detected by the input detector circuit 107 to appear as an input-detector output signal 153 having a waveform as shown in Fig. 15C. The error detector circuit 106 detects the error voltage by comparing the output-detector output signal 152 with the input-detector output signal 153 using the latter output signal 153 as the reference signal and, after amplifying the detected error voltage, outputs the error voltage signal. This error voltage signal is inputted as a gain control signal 154 having a waveform as shown in Fig. 15D to the variable gain control circuit 101 through the gain control terminal 112 so as to control the gain or attenuation. At this time, a power supply control signal 155 having a waveform as shown in Fig. 15E and corresponding to a burst-like high-frequency signal inputted from the portable radio telephone set 114 is inputted through the power supply control terminal 141, so that the output signal of the transmission circuit is on/off controlled to minimize consumption of unnecessary power.



Thus, the ninth embodiment of the present invention in which the power supply control terminal 141 is additionally connected to the power amplifier circuit 102 is advantageous in that the output signal of the transmission circuit is on/off controlled by the burst-like power supply control signal 155 so that consumption of unnecessary power can be minimized. In this ninth embodiment too, the high-frequency amplifier 131 may be connected between the variable high-frequency attenuator 104 and the output detector circuit 105 as in the case of the eighth embodiment.

Fig. 16 is a block diagram showing the structure of a tenth embodiment of the transmission circuit according to the present invention, and Figs. 17A to 17E show waveforms of various signals appearing in the transmission circuit. This tenth embodiment is a partial modification of the seventh embodiment shown in Fig. 11, and the same reference numerals are used to designate the same or equivalent parts appearing in Fig. 11 so as to dispense with repetition of the same description. This tenth embodiment differs from the seventh embodiment in that a power supply control terminal 161 is additionally connected to the power amplifier circuit 102, and a timing generator circuit 162 receiving the output signal of the input detector circuit 107 as its input signal is additionally provided.

The operation of the tenth embodiment of the transmission circuit of the present invention having the above structure will now be described by reference to Fig. 16 together with Figs. 17A to 17E. A high-frequency signal 171 having a waveform as shown in Fig. 17A is inputted from the portable radio telephone set 114 through the high-frequency input terminal 109 and then passed through the input monitor circuit 104. The output signal of the input monitor circuit 104 is, after being amplified by the combination of the variable gain control circuit 101 and the power amplifier circuit 102 and passed through the output monitor circuit 103, appears at the transmission output terminal 110 as the output signal of the transmission circuit.

On the other hand, part of the output signal of the transmission circuit is branched by the output monitor circuit 103 to be inputted as the monitor signal to the variable high-frequency attenuator 104 and, after being attenuated by a predetermined amount by the control signal inputted through the attenuation control terminal 111, inputted to and detected by the output detector circuit 105 to appear as an output-detector output signal 172 having a waveform as shown in Fig. 17B. Also, part of the input signal 171 branched by the input monitor circuit 108 is inputted to and detected by the input detector circuit 107 to appear as an input-detector output signal 173 having a waveform as shown in Fig. 17C. The error detector circuit 106 detects the

error voltage by comparing the output-detector output signal 172 with the input-output signal 173 using the latter output signal 173 as a reference signal and outputs the error voltage signal. This error voltage signal is inputted as a gain control signal 175 having a waveform as shown in Fig. 17E to the variable gain control circuit 101 through the gain control terminal 112 so as to control the gain or attenuation.

The input-detector output signal 173 outputted from the input detector circuit 107 is inputted to the timing generator circuit 162 to appear as a power supply control signal 174 having a waveform as shown in Fig. 17D and turned on/off at certain rising and falling timing of the waveform of the burst-like high-frequency signal 171 inputted from the portable radio telephone set 114. This burst-like power supply control signal 174 is inputted through the power supply control terminal 161 to the power amplifier circuit 102 so as to control the power supply of the power amplifier circuit 102.

Thus, according to the tenth embodiment of the present invention, the burst-like power control signal 174 inputted to the power amplifier circuit 102 through the power supply control terminal 161 is generated on the basis of the high-frequency signal 171, so that a signal line extending from the portable radio telephone set 114 is unnecessary, and the above manner of power supply control can minimize consumption of unnecessary power. In this tenth embodiment too, the high-frequency amplifier 131 used in the eighth embodiment may be connected between the variable high-frequency attenuator 107 and the output detector circuit 105.

## Claims

1. A transmission circuit comprising:
  - power amplifier means (11) for amplifying a high-frequency signal inputted through a high-frequency signal input terminal (18);
  - output monitor means (12) for outputting an output signal of said power amplifier means as an output signal of said transmission circuit and for outputting part of the output signal of said transmission circuit as a monitor signal;
  - output detector means (13) for receiving an output signal of said output monitor means as its input signal and for subjecting the high-frequency signal to envelope detection thereby generating an output signal representing a result of the envelope detection;
  - peak hold means (14) for receiving an output signal of said output detector means as its input signal and for successively holding peaks of the output signal of said output detector means;
  - reference signal generator means (16) for

generating a voltage signal for setting a reference level of the output signal of said transmission circuit; and

error detector means (15) for comparing an output signal of said peak hold means with an output signal of said reference signal generator means to detect an error voltage, an output signal of said error detector means being based to control a gain of said power amplifier means.

2. A transmission circuit according to Claim 1, wherein variable high-frequency attenuator means (31) is connected between said output monitor means (12) and said output detector means (13).
3. A transmission circuit according to Claim 2, wherein high-frequency amplifier means (41) is connected between said variable high-frequency attenuator means (31) and said output detector means (13).
4. A transmission circuit according to Claim 1, 2 or 3, wherein said transmission circuit is used in a booster unit (52) connected to a portable radio telephone set (51) operable as an independent device for amplifying a high-frequency signal transmitted from said portable radio telephone set, said transmission circuit including a power supply control terminal (53) additionally connected to said power amplifier means (11), so that a power supply voltage supplied to said power amplifier means (11) can be switched over in response to a transmission on/off signal corresponding to a transmission burst signal transmitted from said portable radio telephone set.
5. A transmission circuit according to Claims 1, 2 or 3, wherein said transmission circuit is used in a booster unit (72) connected to a portable radio telephone set (71) operable as an independent device for amplifying a high-frequency signal transmitted from said portable radio telephone set, said transmission circuit including input detector means (73) for branching part of a transmission burst signal transmitted from said portable radio telephone set through said high-frequency signal input terminal (18) to receive a branched part of the burst signal as its input signal and for subjecting the high-frequency signal to envelope detection to generate an output signal representing a result of the envelope detection, and timing generator means (74) for receiving an output signal of said input detector means as its input signal and for generating a timing signal correspond-

ing to the output signal of said input detector means, an output signal of said timing generator means being used for switching a power supply voltage supplied to said power amplifier means.

6. A transmission circuit according to Claim 5, wherein a high-frequency amplifier means (91) is connected between said high-frequency signal input terminal (18) and said input detector means (73).
7. A transmission circuit for use in a booster unit (113) connected to a portable radio telephone set (114) operable as an independent device for amplifying a high-frequency signal transmitted from said portable radio telephone set, said transmission circuit comprising:
  - input monitor means (108) for receiving the high-frequency signal transmitted from said portable radio telephone set as its input signal and for outputting part of the high-frequency signal as a monitor signal;
  - variable gain control means (101) for receiving an output signal of said input monitor means as its input signal so as to make variable a gain or attenuation;
  - power amplifier means (102) for receiving an output signal of said variable gain control means as its input signal to amplify the high-frequency signal;
  - output monitor means (103) for generating an output signal of said power amplifier means as an output signal of said transmission circuit and for outputting part of the output signal of said power amplifier means as a monitor signal;
  - variable high-frequency attenuator means (104) for receiving an output signal of said output monitor means as its input signal to attenuate the high-frequency signal and for making variable an amount of attenuation in response to an external control signal;
  - output detector means (105) for receiving an output signal of said variable high-frequency attenuator means as its input signal and for subjecting the high-frequency signal to envelope detection to generate an output signal representing a result of the envelope detection;
  - input detector means (107) having at least one output terminal, said input detector means receiving a monitor signal outputted from said input monitor means as its input signal and for subjecting the high-frequency signal to envelope detection to generate an output signal representing the result of the envelope detection; and
  - error detector means (106) for comparing

an output signal of said output detector means  
with an output signal of said input detector  
means to detect an error voltage, an output  
signal of said error detector means being used  
to control a gain of said power amplifier  
means. 5

8. A transmission circuit according to Claim 7,  
wherein a high-frequency amplifier means  
(131) is connected between said variable high-  
frequency attenuator means (104) and said  
output detector means (105). 10

9. A transmission circuit according to Claim 7 or  
8, wherein a power supply control terminal  
(141) is additionally connected to said power  
amplifier means (102) so that the power supply  
voltage supplied to said power amplifier means  
can be switched over by a transmission on/off  
signal corresponding to a transmission burst  
signal transmitted from said portable radio tele-  
phone set (114). 15 20

10. A transmission circuit according to Claim 7 or  
8, wherein timing generator means (162) for  
receiving the output signal of said input detec-  
tor means (107) as its input signal to generate  
timing corresponding to the input signal is con-  
nected between said input detector means and  
said power amplifier means (102), and an out-  
put signal of said timing generator means is  
used for switching the power supply voltage  
supplied to said power amplifier means. 25 30

11. A transmission circuit comprising: 35  
output detector means (13) for subjecting  
part of an output signal of said transmission  
circuit to envelope detection thereby generat-  
ing an output signal representing a result of  
the envelope detection; 40  
peak hold means (14) for receiving an out-  
put signal of said output detector means as its  
input signal and for successively holding peaks  
of the output signal of said output detector  
means; and 45  
error detector means (15) for comparing  
an output signal of said peak hold means with  
a reference signal to detect an error voltage,  
an output signal of said error detector means  
being used to control a gain of power amplifier  
means (11). 50

55

FIG. 1  
PRIOR ART

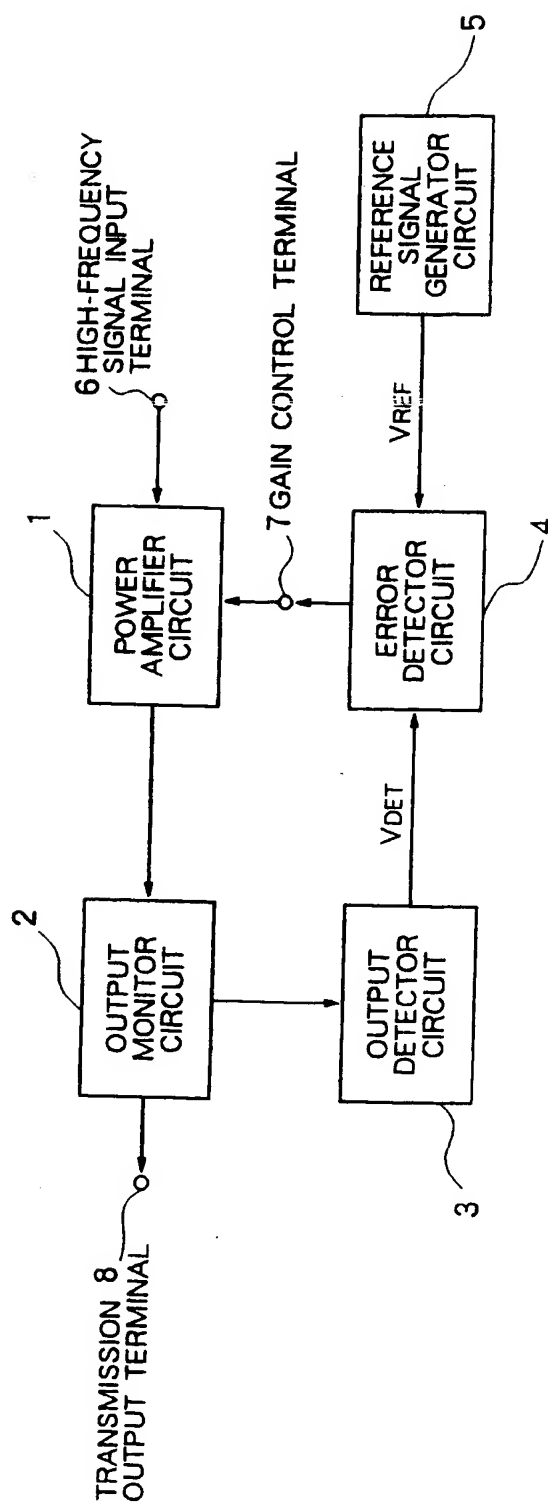


FIG. 2

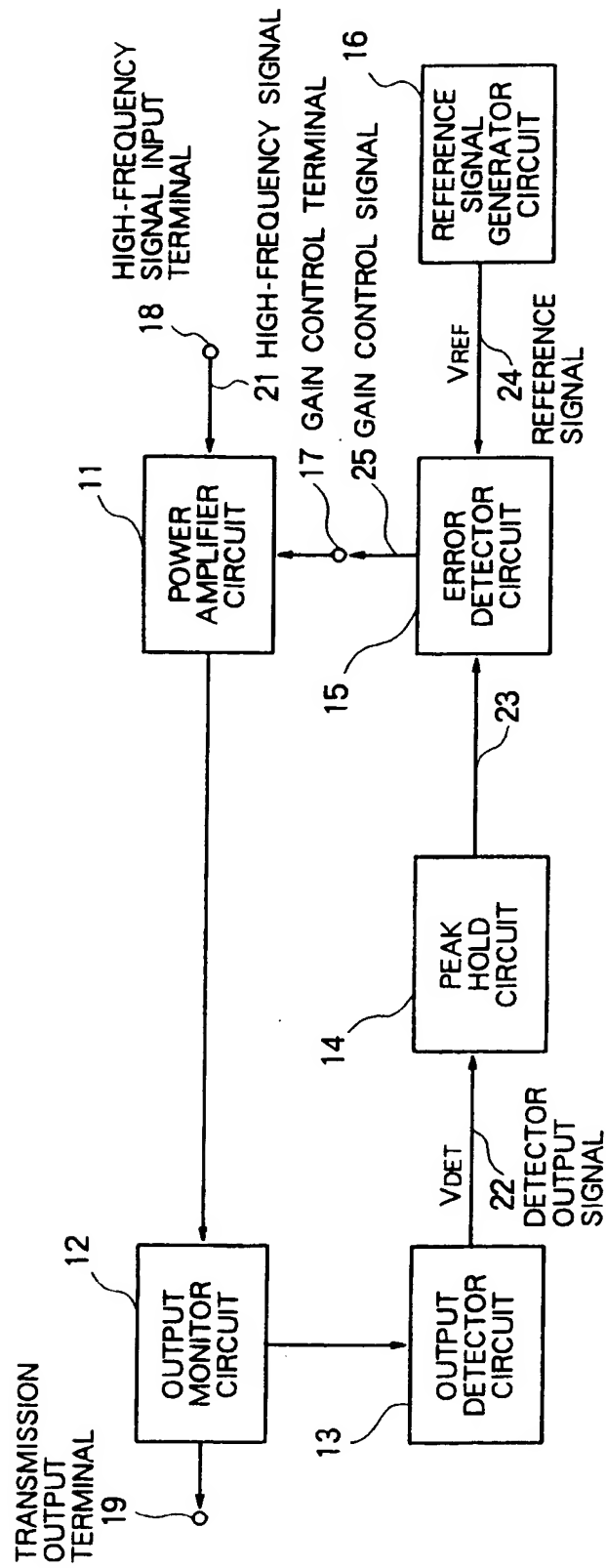


FIG.3A

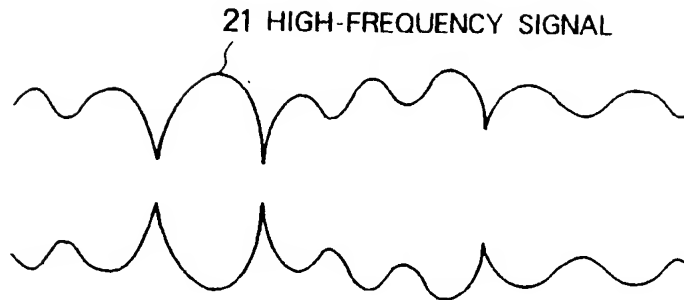


FIG.3B

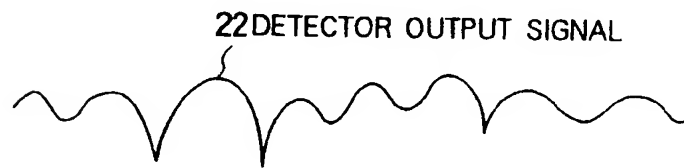


FIG.3C

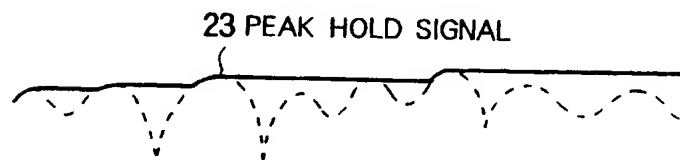


FIG.3D

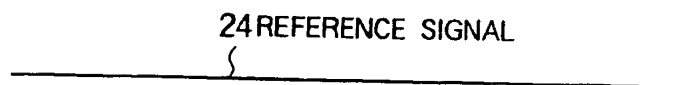


FIG.3E

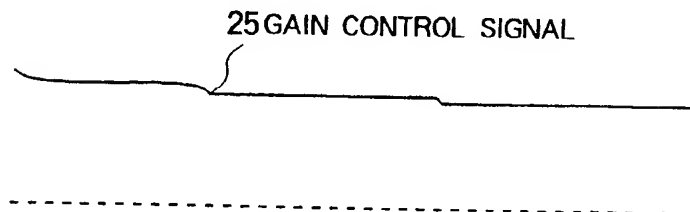


FIG. 4

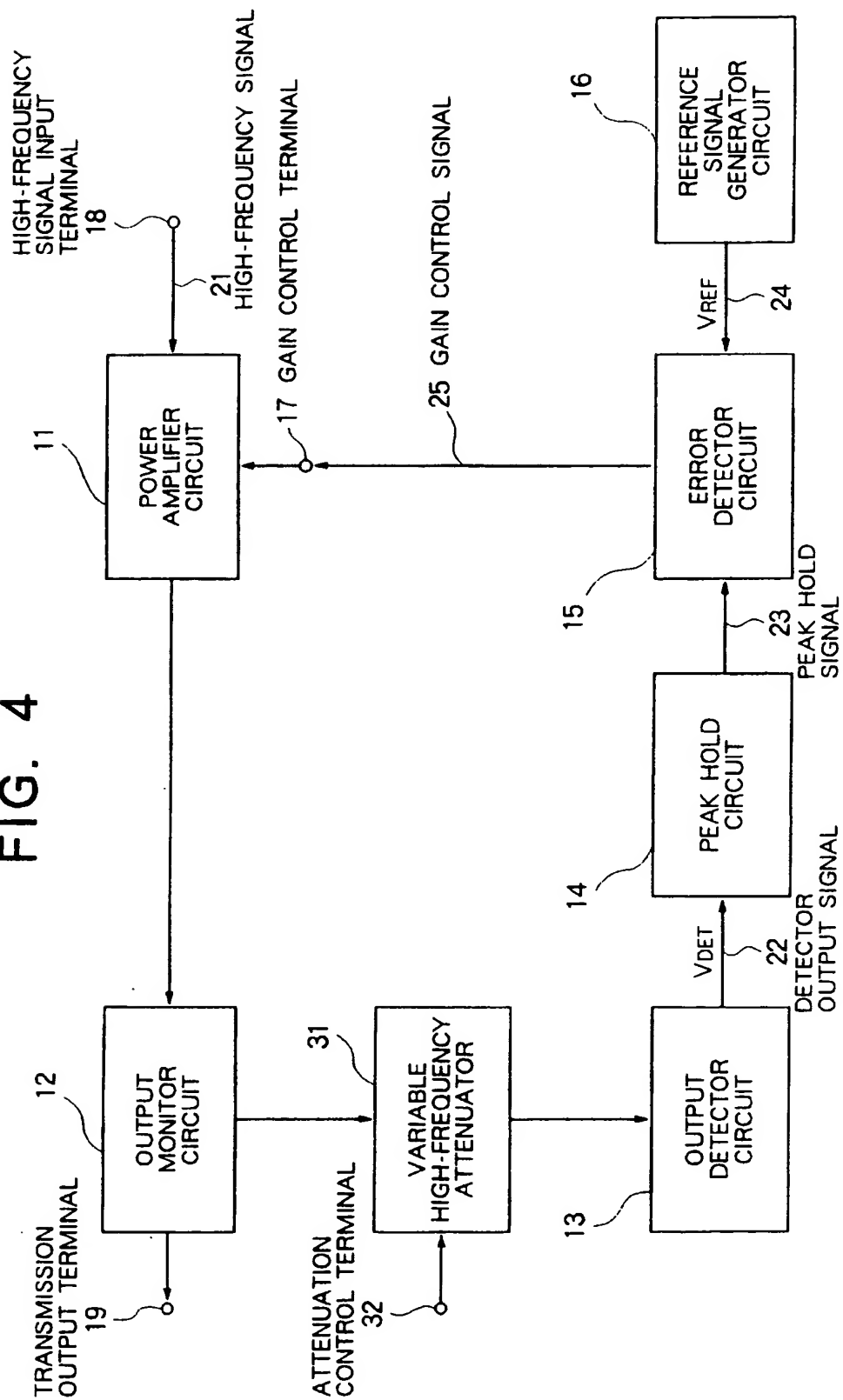


FIG. 5

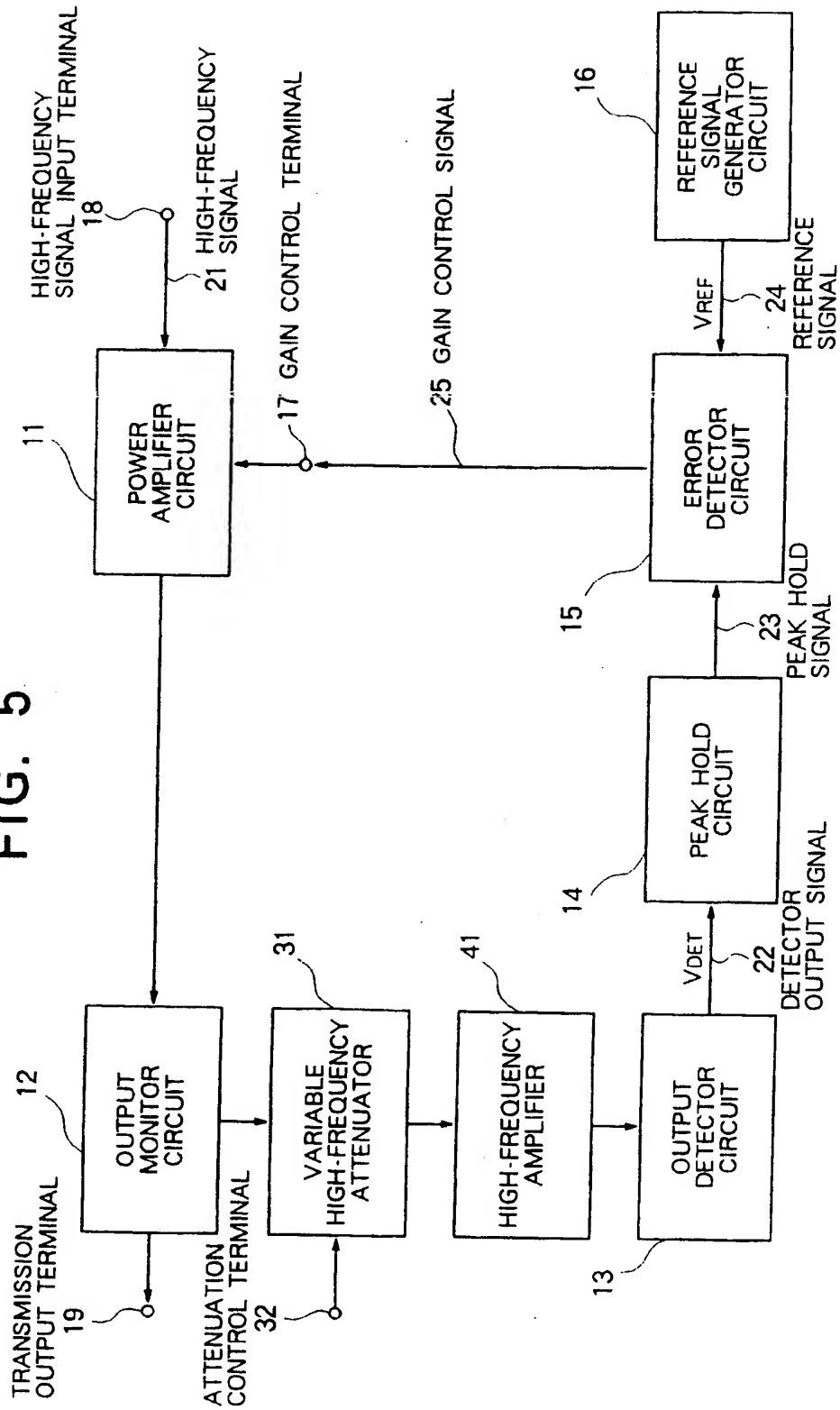




FIG. 6

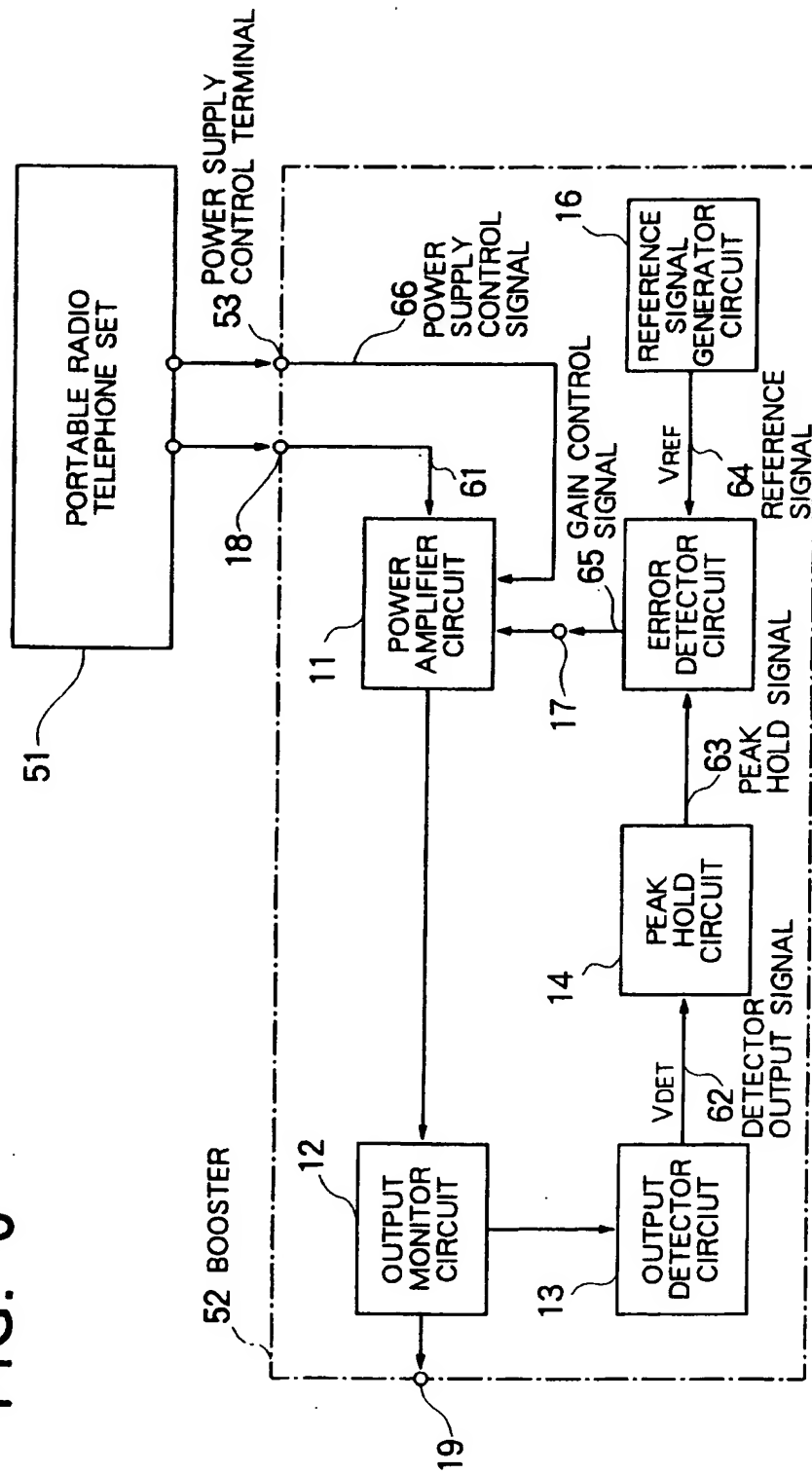


FIG. 7A

61 HIGH-FREQUENCY SIGNAL

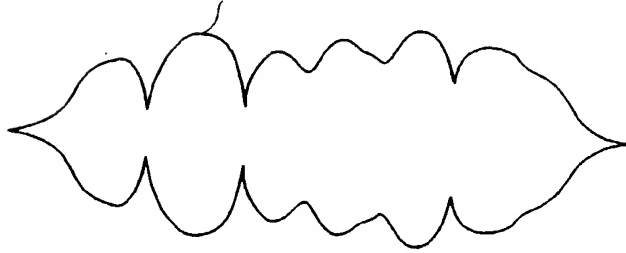


FIG. 7B

62 DETECTOR OUTPUT SIGNAL

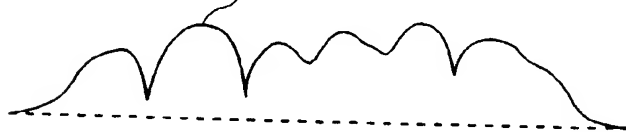


FIG. 7C

63 PEAK HOLD SIGNAL

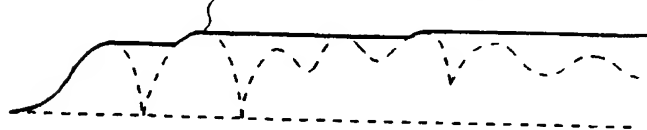


FIG. 7D

64 REFERENCE SIGNAL

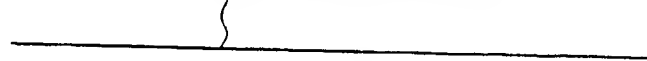


FIG. 7E

65 GAIN CONTROL SIGNAL

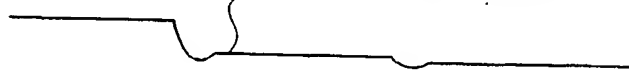
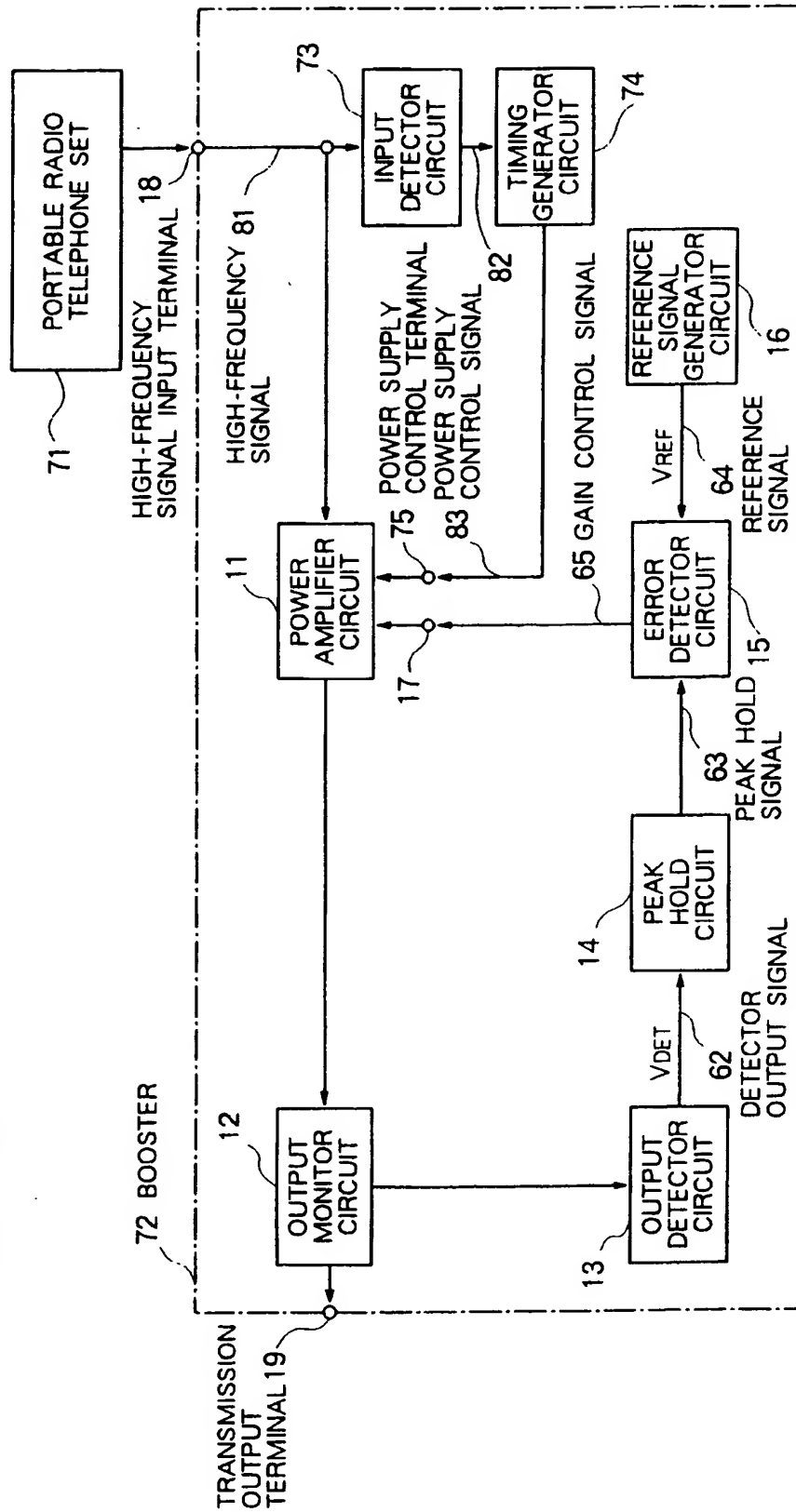


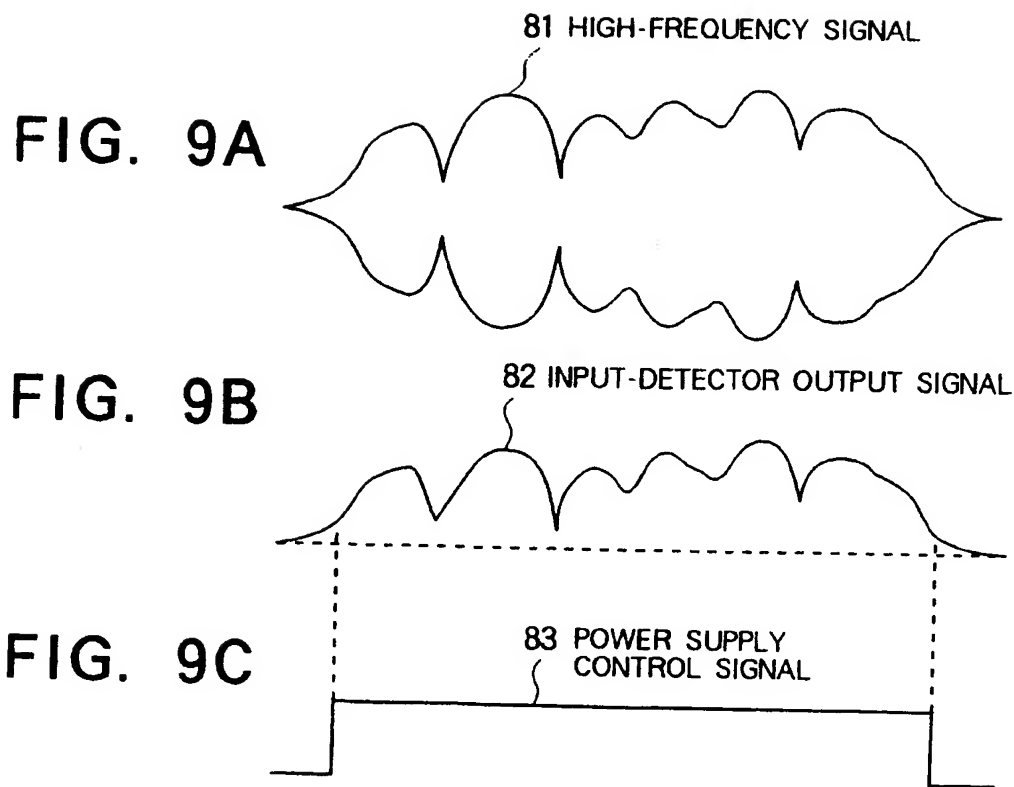
FIG. 7F

66 POWER SUPPLY CONTROL SIGNAL



FIG. 8





21

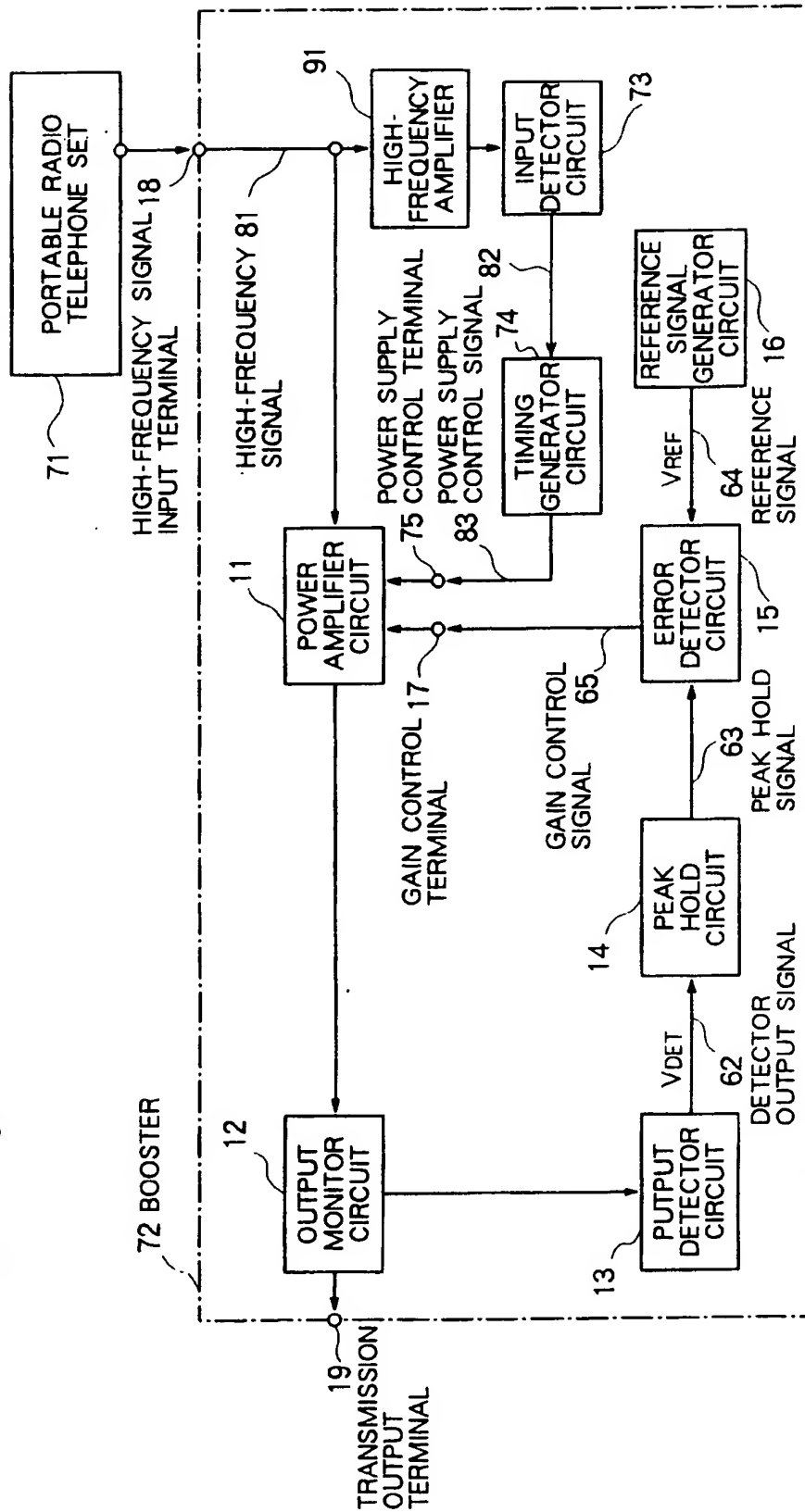
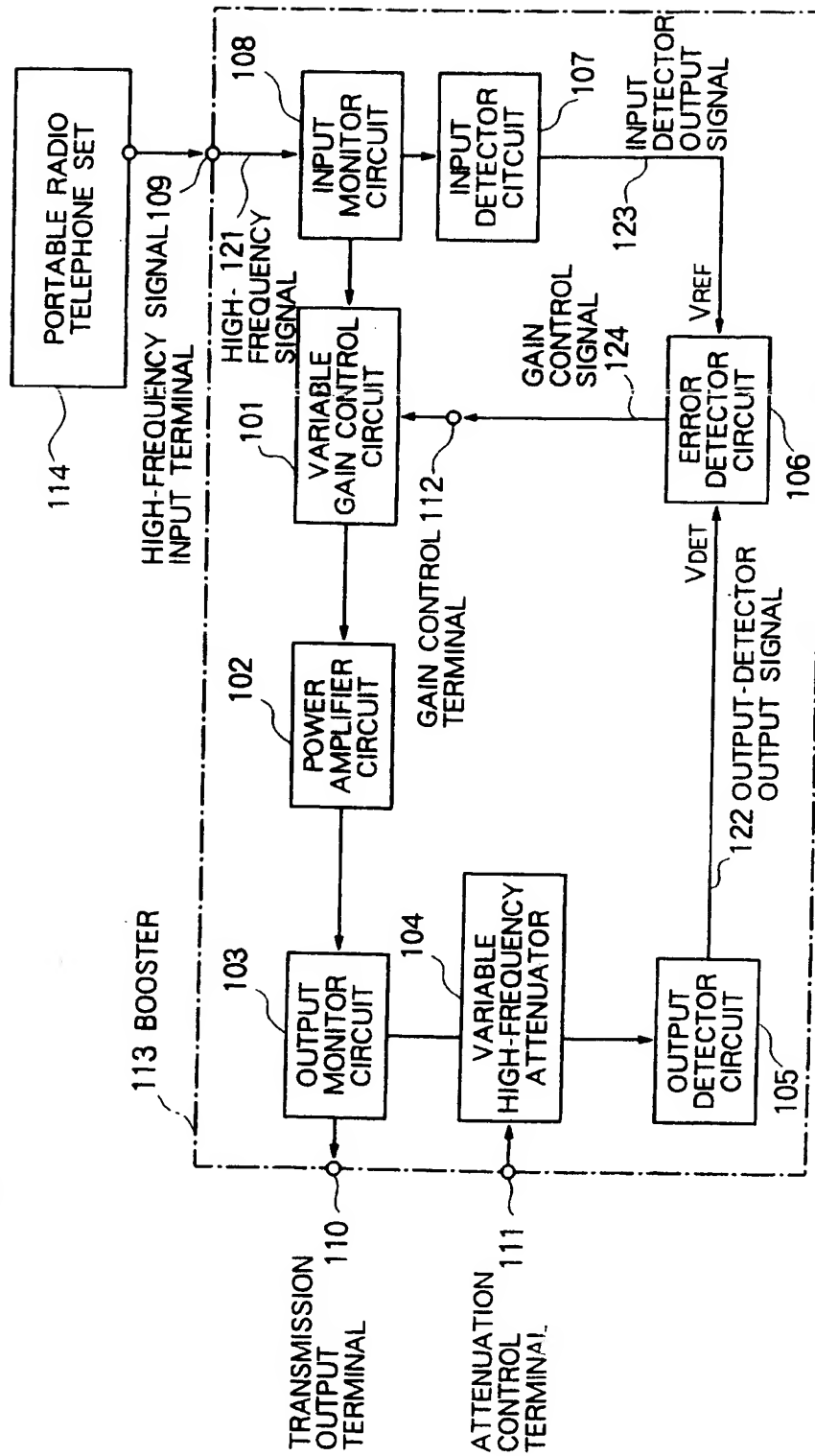


FIG. 11



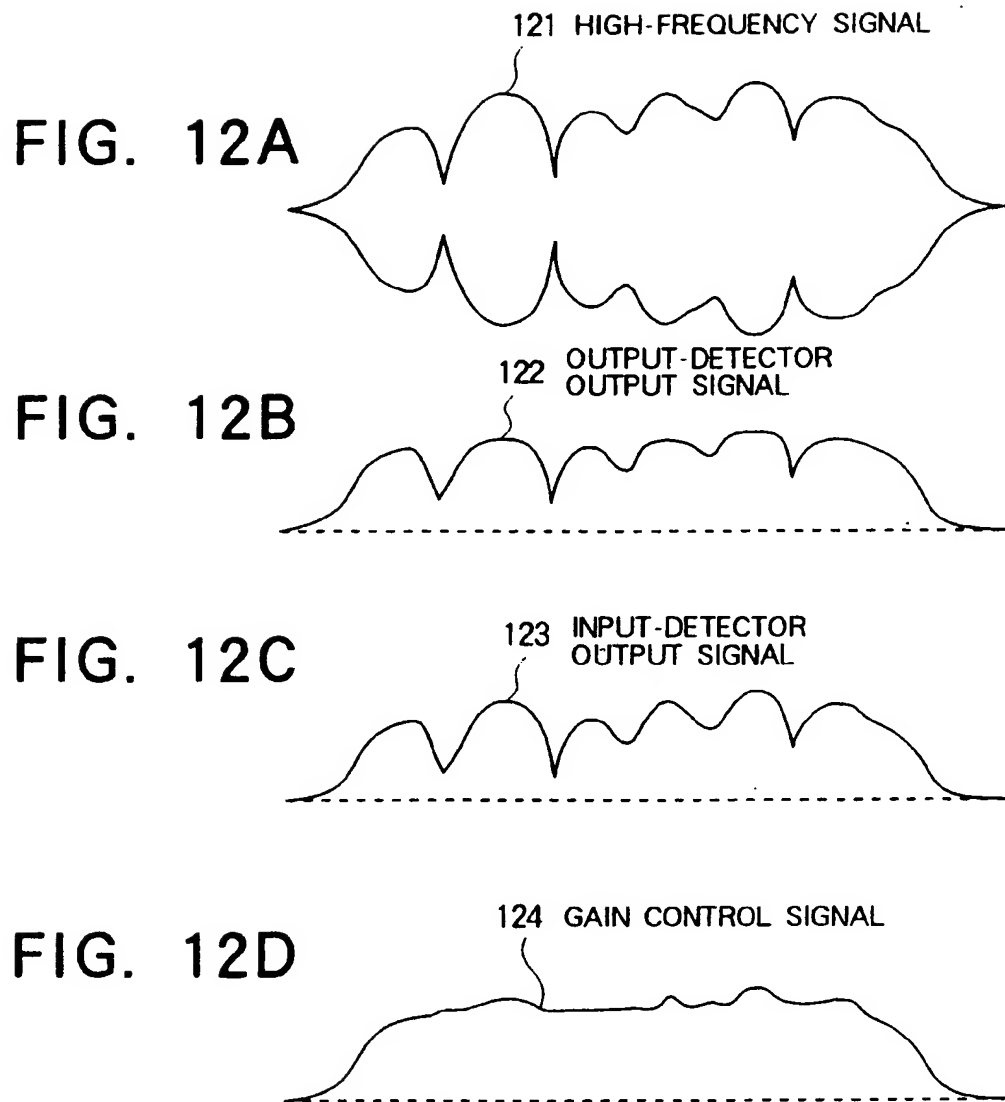
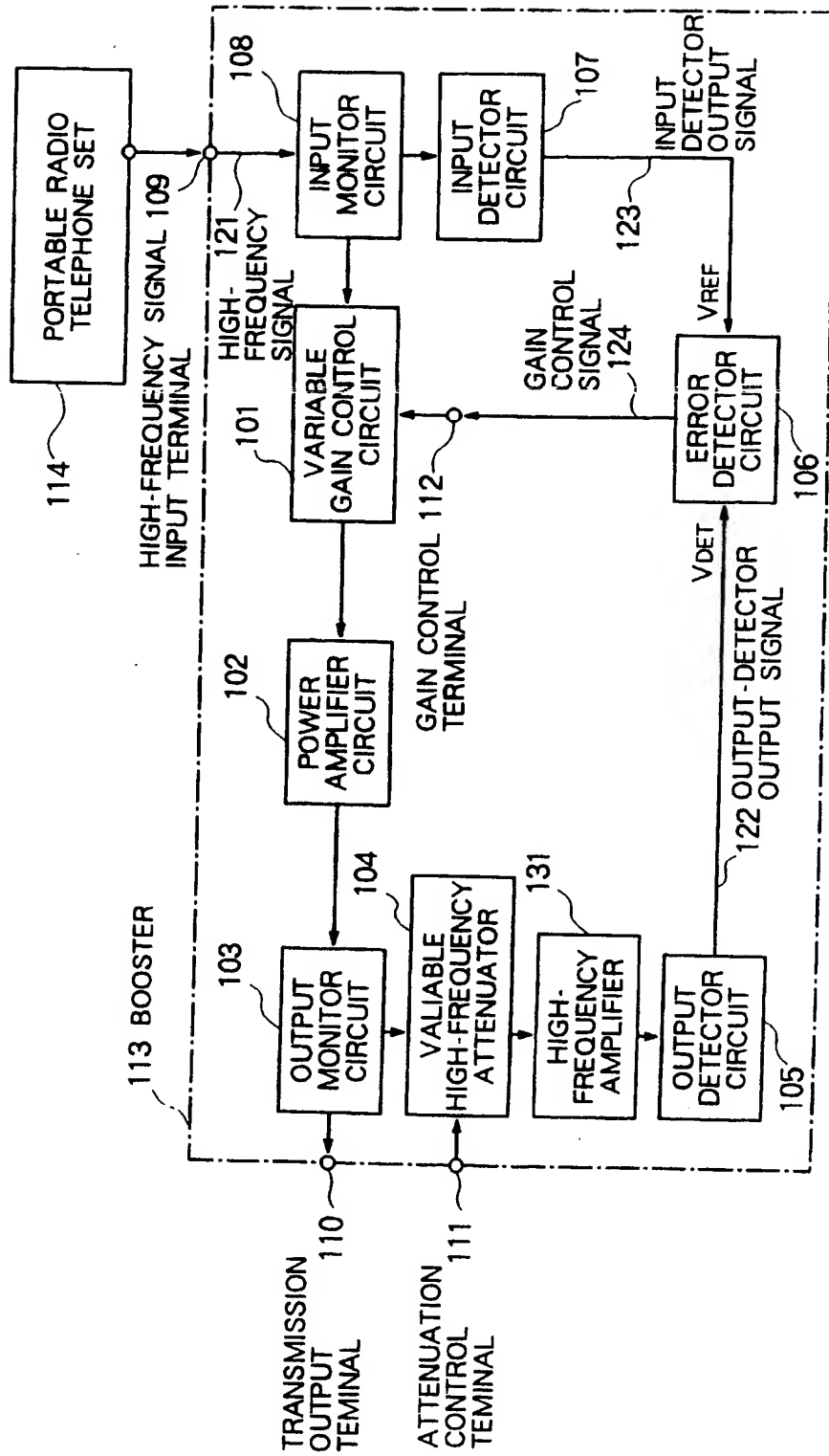


FIG. 13





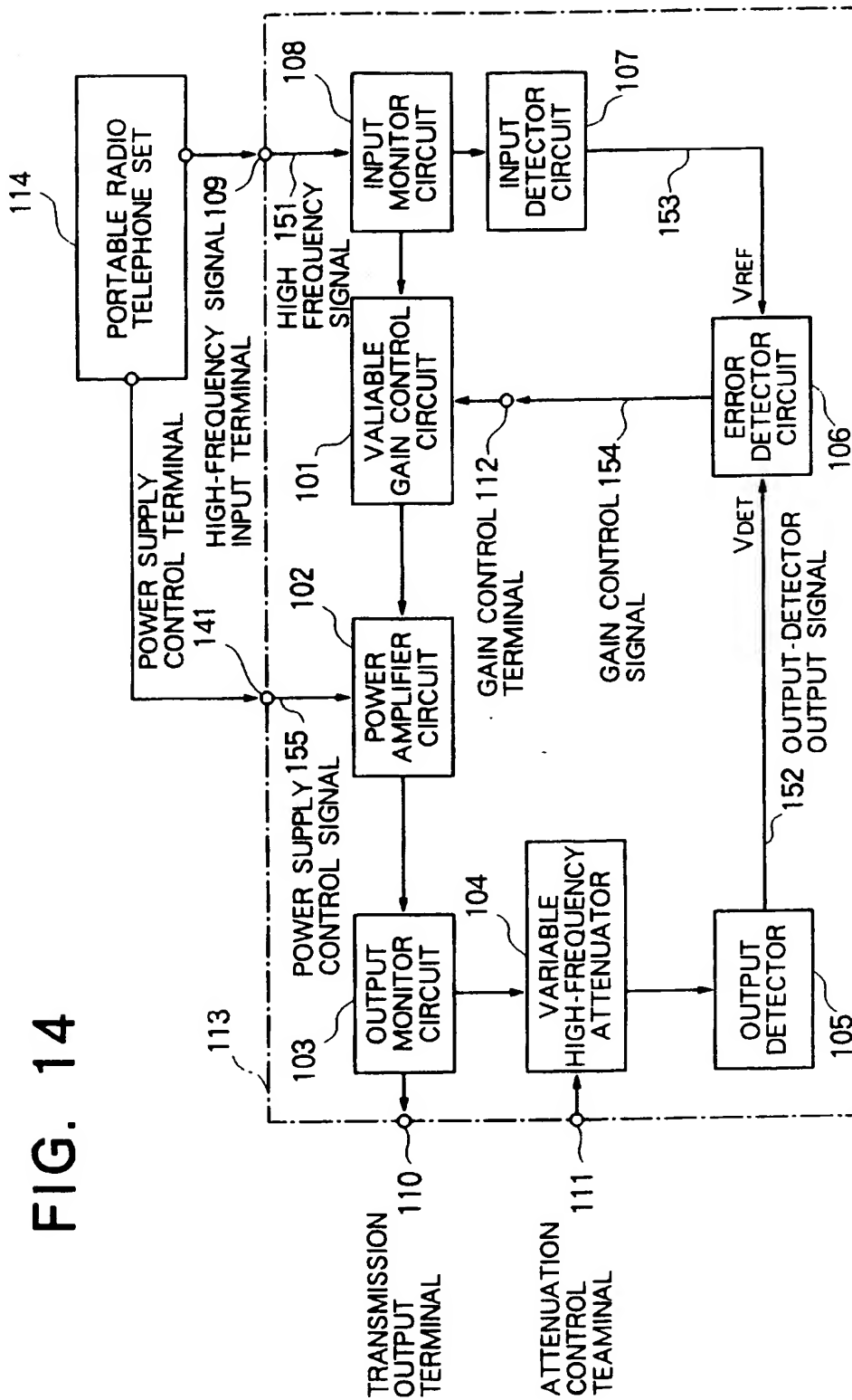


FIG. 15A 151 HIGH-FREQUENCY SIGNAL

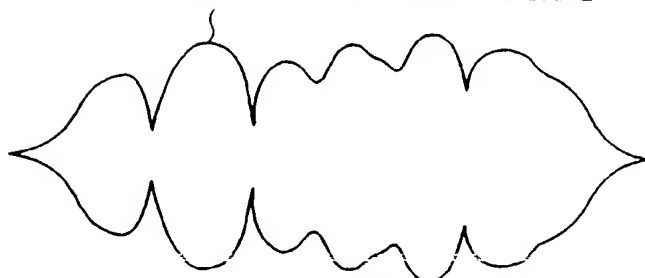


FIG. 15B 152 OUTPUT-DETECTOR OUTPUT SIGNAL

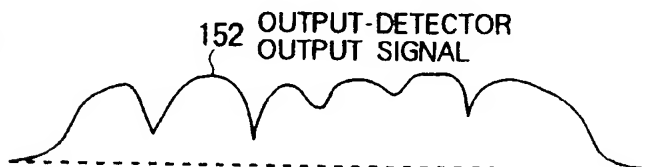


FIG. 15C 153 INPUT-DETECTOR OUTPUT SIGNAL

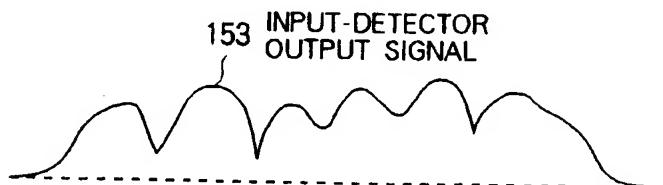


FIG. 15D 154 GAIN CONTROL SIGNAL

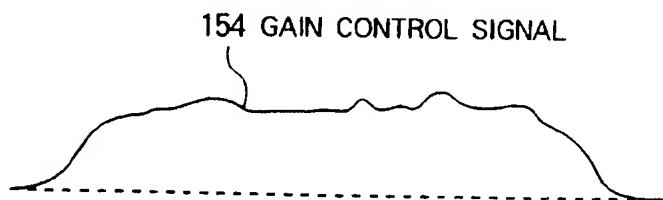
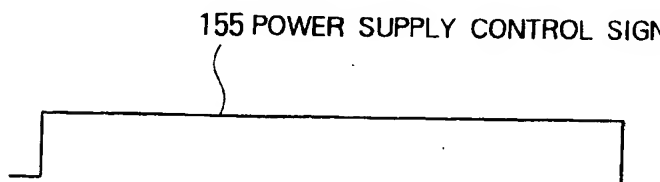


FIG. 15E 155 POWER SUPPLY CONTROL SIGNAL



114



FIG. 17A

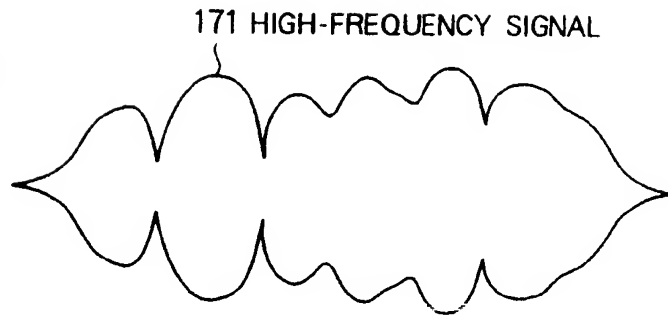


FIG. 17B

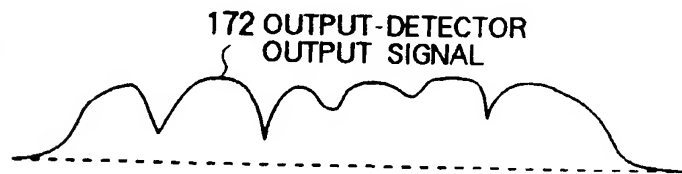


FIG. 17C

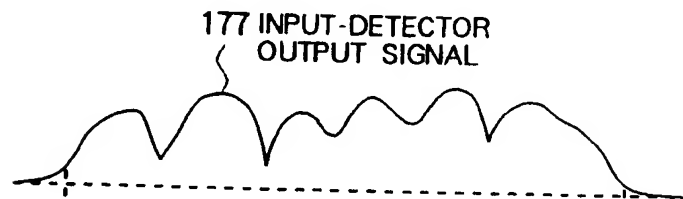


FIG. 17D

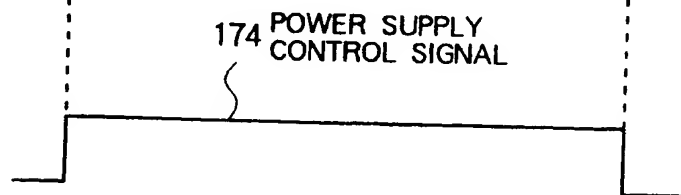
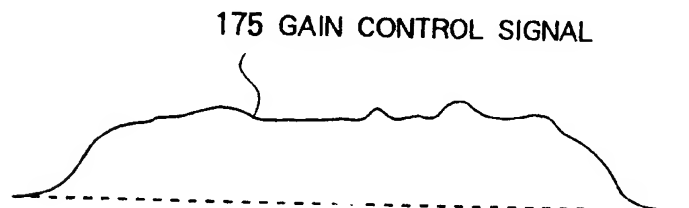
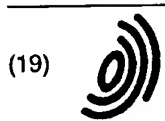


FIG. 17E





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(11)

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(12)

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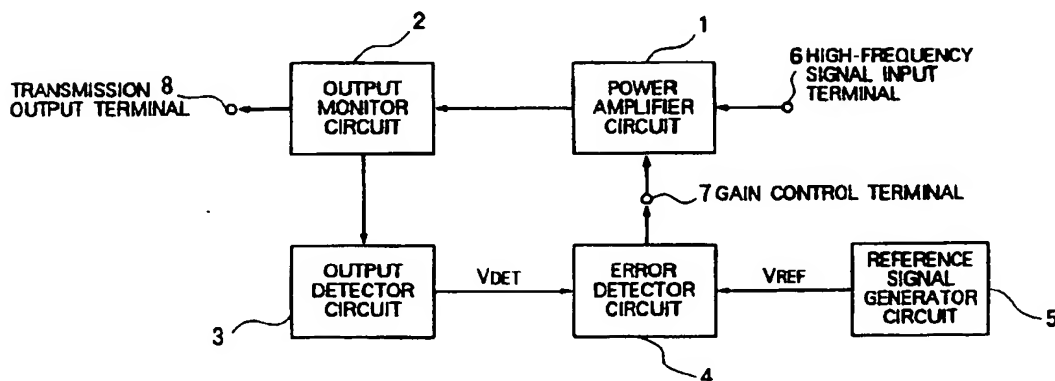
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### (54) Transmission circuit

(57) In the transmission circuit, part of the output signal of the transmission circuit is derived by an output monitor circuit (12), and, after being subjected to envelope detection by an output detector circuit (13), the output signal of the output detector circuit is inputted to a peak hold circuit (14) successively holding the peaks of

the input signal, so that varying amplitude components are removed, and a DC value is detected. An error detector circuit (15) compares the detected value with a reference signal generated from a reference signal generator circuit (16), and the error signal is used as a gain control signal to control the gain of a power amplifier circuit (11).

FIG. 1  
PRIOR ART



EP 0 654 898 A3



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 94 11 8229

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Y	EP-A-0 524 008 (MATSUSHITA ELECTRONICS CORP LT) 20 January 1993 * the whole document *	1-3, 7, 8, 11	H03G3/20
Y	--- PATENT ABSTRACTS OF JAPAN vol. 005 no. 129 (E-070), 19 August 1981 & JP-A-56 066916 (NEC CORP) 5 June 1981, * abstract *	1-3, 11	
Y	--- EP-A-0 159 670 (NIPPON ELECTRIC CO) 30 October 1985 * abstract; figure 1 *	7, 8	
A	--- PATENT ABSTRACTS OF JAPAN vol. 004 no. 067 (E-011), 20 May 1980 & JP-A-55 035514 (MITSUBISHI ELECTRIC CORP) 12 March 1980, * abstract *	1	
A	--- FR-A-2 690 021 (ALCATEL TELSPACE) 15 October 1993 * abstract *	1, 11	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H03G H04B
Place of search THE HAGUE		Date of completion of the search 30 November 1995	Examiner Blaas, D-L
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